



## Hervé BRUNELIERE

### PSAM12 Speaker Bio

## How to integrate correctly hardware common cause failures in frequency calculations?

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**Short Statement:** First author

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### BIOGRAPHY

- Graduate engineer of Ecole Supérieure d'Electricité (SUPELEC) in 2000.

- "AREVA expert" in safety and licensing for I&C (Instrumentation&Control).

- Worked as a consultant for risk (nuclear, oil and gas) industries in ISO Ingénierie and Hemisphères between 2002 and 2005 on projects connected to the I&C, to the safety/security and on applications of IEC 61508 and IEC 61511.

- Joined the safety department of AREVA NP SAS at the end of 2005 where he works mainly for the different EPR projects, for French installed fleet and for ASTRID (research sodium reactor), on:

- 1) I&C systems FMEAs and reliability and availability studies (digital and hardware systems).
- 2) Integration of I&C in the PSAs.
- 3) Deterministic and probabilistic safety requirements for I&C architecture and for I&C systems design.
- 4) Normative aspects connected to the I&C in nuclear power plants.

- Member of working group WGRISK/DIGREL developing best practice guidelines on failure modes taxonomy for reliability assessment of digital I&C systems for PSA.

- Member of working groups 3 (Application of digital processors to safety in nuclear power plants) and 7 (Reliability of electrical equipment in reactor safety systems) of SC45A (experts for IEC activities on nuclear power plants).

- Member of IMDR (Institut de la Maîtrise des Risques), institute grouping French risk management and reliability experts.

- Member of the program committee of Lambda-mu 19 conference (Dijon, October 2014).

